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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

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Serial No.: 09/099,742

Group Art Unit: 2663

Filing Date: June 18, 1998

Examiner: I. Mehra

For: SYSTEM AND METHOD USING A PACKETIZED ENCODED BITSTREAM
FOR PARALLEL COMPRESSION AND DECOMPRESSION

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Technology Center 2600

EXPRESS MAIL LABEL NO: EL695382935US
DATE OF DEPOSIT: AUGUST 9, 2002Commissioner for Patents
Washington, DC 20231AMENDMENT

In response to the Office Action dated May 9, 2002 (paper no. 7), please reconsider the application in view of the following amendment and remarks.

In the specification:

Please replace the paragraph beginning at page 17, line 5, with the following rewritten paragraph:

--With respect to component interleaving, fixed-length packetization can be done on a VxH rectangular region of pixels. Pixels generally consist of one or more components. The tag bits can be used to distribute the packets to different decoders, and the tag bits represent different scan lines. Within each scan line, the data can be encoded by interleaving blocks of components. This interleaving scheme can be the same one used for conventional LCF. Having the packetization scheme use the same interleaving pattern helps to simplify the logic that will be used to encode/decode both LCF and packetized LCF. For example, when encoding pixels which have four fully sampled components (RGBA, 4:4:4:4), there would be block of R, then a block of G, then a block of B, and finally a block of A before moving on to the next set of pixels. It should be noted that